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*****
337435 Fri May  3 08:30:29 2019
new/usr/src/uts/common/io/sata/adapters/ahci/ahci.c
10055 recursive mutex enter in ahci
*****
_____ unchanged_portion_omitted_


1123 /*
1124  * The detach entry point for dev_ops.
1125  */
1126 static int
1127 ahci_detach(dev_info_t *dip, ddi_detach_cmd_t cmd)
1128 {
1129     ahci_ctl_t *ahci_ctlp;
1130     int instance;
1131     int ret;
1132
1133     instance = ddi_get_instance(dip);
1134     ahci_ctlp = ddi_get_soft_state(ahci_statep, instance);
1135
1136     AHCIDBG(AHCIDBG_ENTRY, ahci_ctlp, "ahci_detach enter", NULL);
1137
1138     switch (cmd) {
1139     case DDI_DETACH:
1140
1141         /* disable the interrupts for an uninterrupted detach */
1142         mutex_enter(&ahci_ctlp->ahcictl_mutex);
1143         ahci_disable_all_intrs(ahci_ctlp);
1144         mutex_exit(&ahci_ctlp->ahcictl_mutex);
1145
1146         /* unregister from the sata framework. */
1147         ret = ahci_unregister_sata_hba_tran(ahci_ctlp);
1148         if (ret != AHCI_SUCCESS) {
1149             mutex_enter(&ahci_ctlp->ahcictl_mutex);
1150             ahci_enable_all_intrs(ahci_ctlp);
1151             mutex_exit(&ahci_ctlp->ahcictl_mutex);
1152         }
1153
1154         ahci_em_fini(ahci_ctlp);
1155
1156         mutex_enter(&ahci_ctlp->ahcictl_mutex);
1157
1158         /* stop the watchdog handler */
1159         (void) untimeout(ahci_ctlp->ahcictl_timeout_id);
1160         ahci_ctlp->ahcictl_timeout_id = 0;
1161
1162         mutex_exit(&ahci_ctlp->ahcictl_mutex);
1163
1164         /* uninitialized the controller */
1165         ahci_uninitialize_controller(ahci_ctlp);
1166
1167         /* remove the interrupts */
1168         ahci_rem_intrs(ahci_ctlp);
1169
1170         /* deallocate the ports structures */
1171         ahci_dealloc_ports_state(ahci_ctlp);
1172
1173         /* destroy mutex */
1174         mutex_destroy(&ahci_ctlp->ahcictl_mutex);
1175
1176         /* teardown the pci config */
1177         pci_config_teardown(&ahci_ctlp->ahcictl_pci_conf_handle);
1178
1179         /* remove the reg maps. */
1180         ddi_regs_map_free(&ahci_ctlp->ahcictl Ahci_acc_handle);
1181

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1183     /* release fma resource */
1184     ahci_fm_fini(ahci_ctlp);
1185
1186     /* free the soft state. */
1187     ddi_soft_state_free(ahci_statep, instance);
1188
1189     return (DDI_SUCCESS);
1190
1191     case DDI_SUSPEND:
1192
1193         /*
1194          * The steps associated with suspension must include putting
1195          * the underlying device into a quiescent state so that it
1196          * will not generate interrupts or modify or access memory.
1197          */
1198         mutex_enter(&ahci_ctlp->ahcictl_mutex);
1199         if (ahci_ctlp->ahcictl_flags & AHCI_SUSPEND) {
1200             mutex_exit(&ahci_ctlp->ahcictl_mutex);
1201             return (DDI_SUCCESS);
1202         }
1203
1204         ahci_ctlp->ahcictl_flags |= AHCI_SUSPEND;
1205
1206         ahci_em_suspend(ahci_ctlp);
1207
1208         /* stop the watchdog handler */
1209         if (ahci_ctlp->ahcictl_timeout_id) {
1210             (void) untimeout(ahci_ctlp->ahcictl_timeout_id);
1211             ahci_ctlp->ahcictl_timeout_id = 0;
1212         }
1213
1214         mutex_exit(&ahci_ctlp->ahcictl_mutex);
1215
1216         ahci_em_suspend(ahci_ctlp);
1217
1218         /*
1219          * drain the taskq
1220          */
1221         ahci_drain_ports_taskq(ahci_ctlp);
1222
1223         /*
1224          * Disable the interrupts and stop all the ports.
1225          */
1226         ahci_uninitialize_controller(ahci_ctlp);
1227
1228         return (DDI_SUCCESS);
1229
1230     default:
1231         return (DDI_FAILURE);
1232     }
1233 }
_____ unchanged_portion_omitted_
```